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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Application of: DeWitt, Jr. et al.

Serial No.: 10/757,197

Filed: January 14, 2004

For: **Method and Apparatus for
Optimizing Code Execution Using
Annotated Trace Information Having
Performance Indicator and Counter
Information**

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Group Art Unit: 2192

Examiner: **Dam, Tuan Quang**

Attorney Docket No.: AUS920030556US1

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By: Louise Fay
Louise Fay

35525

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Commissioner for Patents
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Alexandria, VA 22313-1450

Sir:
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- Form PTO-1449;
- References AG-AI; and
- Our return postcard.

No fees are believed to be necessary. If, however, any fees are required, I authorize the Commissioner to charge these fees which may be required to Deposit Account No. 09-0447. No extension of time is believed to be necessary. If, however, an extension of time is required, the extension is requested, and I authorize the Commissioner to charge any fees for this extension to Deposit Account No. 09-0447.

Respectfully submitted,

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

DeWitt, Jr. et al.

Serial No: **10/757,197**

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Title: **Method and Apparatus for Optimizing
Code Execution Using Annotated Trace
Information Having Performance Indicator
and Counter Information**

Group Art Unit: **2192**

Intellectual Property
Law Department
International Business
Machines Corporation
11400 Burnet Road
Austin, Texas 78758

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicants submit herewith patents, publications or other information of which they are aware, which they believe may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 C.F.R. 1.56.

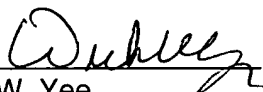
While this Supplemental Information Disclosure Statement may be "material" pursuant to 37 C.F.R. 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" for this invention unless specifically designated as such.

In accordance with 37 C.F.R. 1.97(g) the filing of this Supplemental Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR 1.56 (b) exists.

No item of information cited in this Supplemental Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in this Supplemental Information Disclosure Statement was known to any individual designated in § 1.56(c) more than three months prior to the filing of this Supplemental Information Disclosure Statement.

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Respectfully submitted,



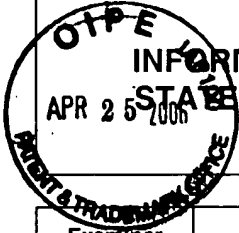
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Substitute for form 1449/PTO

Complete if Known

Application Number	10/757,197
Filing Date	01-14-2004
First Named Inventor	DeWitt, Jr. et al.
Examiner Name	Dam, Tuan Quang
Art Unit	2192
Attorney Docket No.	AUS920030556US1



**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
AA	5,797,019	08-18-1998	Levine et al.	
AB	6,549,998 B1	04-15-2003	Pekarich et al.	
AC	2003/0101367 A1	05-29-2003	Bartfai et al.	
AD	5,113,507	05-12-1992	Jaekel	
AE	6,681,387 B1	01-20-2004	Hwu et al.	
AF	6,925,424 B2	08-02-2005	Jones et al.	

FOREIGN PATENT DOCUMENTS

Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

NON PATENT LITERATURE

Examiner Initial	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
AG	Wikipedia "JavaServer Pages" downloaded 1/24/06 http://en.wikipedia.org/wiki/JavaServer_Pages
AH	"Hardware Cycle Based memory Residency", IBM, May 22, 2003, ip.com, IPCOM000012728D, pp. 1-2
AI	HYDE, "The Art of Assembly Language", 2001, Linux Edition, pp. 247-248, retrieved Mar. 1, 2005 from http://webster.cs.ucr.edu/AoA/Linux/PDFs/0_PDFIndexLinux.html

Examiner:

Date Considered:

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